

REMARKS

Present Status of the Application

Claims 8-15 are pending. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Man et al. (US Patent Application No. 2003/0101391, hereinafter "Man") in view of Day et al. (US-5,663,966, hereinafter "Day"). In addition, Claims 9-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

After amending the claims and traversing the rejections, claims 8-15 remain pending in the present application, and reconsideration of those claims is respectfully requested.

Discussion of the claim rejection under 35 USC 103

The Office Action rejected claim 8 under 35 U.S.C. 103(a) as being unpatentable over Man et al. (US Patent Application No. 2003/0101391, hereinafter "Man") in view of Day et al. (US-5,663,966, hereinafter "Day").

Applicant traverses the rejection for reasons discussed below. Nevertheless, Applicant has amended claim 8 to recite "testing an analog circuit of the DVD ROM chipset". The amendment made to claim 8 is fully supported by the specification, for example, by paragraph [0029] and Fig. 2 of the specification of present application.

Man et al. do not teach or suggest testing an analog circuit block of a DVD ROM chipset.

Paragraph [0013] in Man states:

"....Tester 101 runs tester application 107 from memory 105 Memory 105 may include read-only memory, random access memory, floppy disk memory, hard-disk memory, magnetic tape memory, digital versatile disk (DVD) memory, compact disc (CD) memory," As can be clearly seen, the DVD memory in Man is used as the memory for operating the tester, which means that the DVD memory is **PART OF** the Test Apparatus. On the other hand, the "analog circuit of the DVD ROM chipset" recited explicitly in the amended Claim 8 of the present invention is NOT part of the test

apparatus, but is part of the DEVICE TO BE TESTED UPON instead. Therefore, "testing an analog circuit block of DVD ROM chipset" is not taught by Man.

Indeed, the situation in Man is equivalent to "testing a device using a DVD ROM chipset." On the other hand, the amended Claim 8 of the present invention recites testing the "analog circuit of the DVD ROM chipset".

Day et al. do not teach or suggest testing the analog circuit of a DVD ROM chipset either.

Therefore, the amended Claim 8 patentably distinguishes over Man in view of Day, and should be allowed.

Claims 9-15 depend on claims claim 8 and are also allowable.

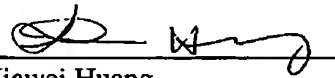
CONCLUSION

For at least the foregoing reasons, it is believed that all the pending Claims 8-15 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date: 12/16/2005

4 Venture, Suite 250
Irvine, CA 92618
Tel.: (949) 660-0761
Fax: (949)-660-0809

Respectfully submitted,
J.C. PATENTS


Jiawei Huang
Registration No. 43,330